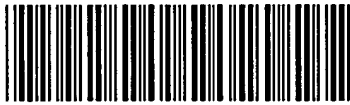


Search Notes

Application/Control No.

10/090,250

Examiner

Ted M. Wang

Applicant(s)/Patent under
Reexamination

MENON ET AL.

Art Unit

2634

SEARCHED

Class	Subclass	Date	Examiner
375	219	9/16/2005	TW
326	39	9/16/2005	TW

INTERFERENCE SEARCHED

Class	Subclass	Date	Examiner

**SEARCH NOTES
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
EAST - USPGPUB, USPAT, USOCR, EPO, JPO, DERWENT, IBM_TDB, see attached search report	9/16/2005	TW
IEEE - Advance search -(integrated circuit) and (transceiver or receiver or driver or sender) with configurable cell memory	9/16/2005	TW
IEEE - Advance search - FPGA and cell memory and loss of synchronization	9/16/2005	TW
GOOGLE - (FPGA or PLA or PLD) and cell memory and loss of synchronization	9/16/2005	TW